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Hardware Verification of Microwave Power Amplifier Models.

ABSTRACT - Masterthesis

This thesis gives an overview of difference in responses for microwave power amplifier modeled and measured circuits. The objective of the thesis is to design active two port circuits using BFP540 transistor in the frequency range from 1-4 GHz.

Initially the Linear/Nonlinear models are designed without matching circuitry and modeled circuits are tested on Rohde & Schwarz VNA through different calibration techniques. After studying the effects of these models a complete single stage amplifier is designed for frequency 2.6 GHz.

The circuits in this thesis are designed and simulated in AWR microwave office.